Ref #	Hits	Search Query	DBs	Default Operator	Plurals
L1	0	semiconductor and (glass adj window) adj3 (linear adj expansion)	USPAT	OR	OFF
L2	0	semiconductor and (glass adj window) adj3 (linear adj3 expansion)	USPAT	OR	OFF
L3	17	semiconductor and (glass adj window) and (linear adj3 expansion)	USPAT	OR ;	OFF
L4	92	semiconductor and glass adj3 (linear adj3 expansion)	USPAT	OR	OFF
L5	. 1	semiconductor and window adj3 (linear adj3 expansion)	USPAT	OR :	OFF
L6	. 1	semiconductor and window adj5 (linear adj3 expansion)	USPAT	OR	OFF
L7	1	(uv adj transparent adj glass)and ROM and (glass adj window)and (linear adj2 expansion)	USPAT	OR	OFF
L8	1	(uv adj transparent adj glass)and ROM and (linear adj2 expansion)	USPAT	OR	OFF
L9	1	(uv adj2 glass)and ROM and (linear adj2 expansion)	USPAT	OR	OFF
L10	86	(glass)and ROM and (linear adj2 expansion)	USPAT	OR	OFF
L11	0	(glass adj3 ROM) and (linear adj2 expansion)	USPAT	OR	OFF
L12	454	(501/59).CCLS.	USPAT; USOCR	OR	OFF
L13	51	501/905.ccls. and window	USPAT	OR	OFF
L14	. 2 :	13 and chips	USPAT	OR	OFF
L15	7	13 and (chips or IC or semiconductor)	USPAT	OR	OFF
L16	602	"501"/\$.ccls. and window	USPAT	OR	OFF
L17	112	16 and (chips or IC or semiconductor)	USPAT	OR	OFF
L18	6	257/680.ccls. and (linear adj expansion)	USPAT	OR	OFF
L19	6	257/680.ccls. and (linear adj2 expansion)	USPAT	OR	OFF
L20	6	257/680.ccls. and (linear adj3 expansion)	USPAT	OR	OFF
L21	0	"501"/\$.ccls. and (linear adj2 exapansion)and chips	USPAT	OR	OFF
L22	32	"501"/\$.ccls. and (linear adj2 expansion)and chips	USPAT	OR	OFF

0 0

i

:

0. . . .

;

L23	22	"501"/\$.ccls. and window and chips	USPAT	OR	OFF
L24	0	(glass adj window)adj (linear adj expansion)	USPAT	OR	OFF
L25	0	(glass adj window) adj2 (linear adj expansion)	USPAT	OR	OFF
L26	0	(glass adj2 window) adj2 (linear adj expansion)	USPAT	OR	OFF
L27	0	(glass adj2 window) adj4 (linear adj expansion)	USPAT	OR	OFF
L28	63	glass and window and package and (linear adj2 expansion)	USPAT	OR	OFF
L29 .	0	semiconductor and (glass adj lens) adj3 (linear adj expansion)	USPAT	OR	OFF
L30	43	semiconductor and (glass adj2 lens) and (linear adj expansion)	USPAT	OR	OFF
L31	1	chips and (glass adj2 lens) and (linear adj expansion)	USPAT	OR	OFF
L32	0	(linear adj expansion adj "120")and window	USPAT	OR	OFF
L33	0	(linear adj expansion adj "100")and window	USPAT	OR	OFF
L34	0	semiconductor and (glass adj window) adj3 (linear adj expansion)	EPO; JPO	OR	OFF
· L35	14	semiconductor and glass adj3 (linear adj3 expansion)	EPO; JPO	OR ·	OFF
L36	0	(glass adj2 window) adj4 (linear adj expansion)	EPO; JPO	OR	OFF
L37	. 0	(glass adj2 window) adj2 (linear adj expansion)	EPO; JPO	OR	OFF
	L24 L25 L26 L27 L28 L29 L30 L31 L32 L33 L34 L35 L36	L24 0 L25 0 L26 0 L27 0 L28 63 L29 0 L30 43 L31 1 L32 0 L33 0 L34 0 L35 14 L36 0	L24 0 (glass adj window)adj (linear adj expansion) L25 0 (glass adj window) adj2 (linear adj expansion) L26 0 (glass adj2 window) adj2 (linear adj expansion) L27 0 (glass adj2 window) adj4 (linear adj expansion) L28 63 glass and window and package and (linear adj2 expansion) L29 0 semiconductor and (glass adj lens) adj3 (linear adj expansion) L30 43 semiconductor and (glass adj2 lens) and (linear adj expansion) L31 1 chips and (glass adj2 lens) and (linear adj expansion) L32 0 (linear adj expansion adj "120")and window L33 0 (linear adj expansion adj "100")and window L34 0 semiconductor and (glass adj window) adj3 (linear adj expansion) L35 14 semiconductor and glass adj3 (linear adj3 expansion) L36 0 (glass adj2 window) adj4 (linear adj expansion) L37 0 (glass adj2 window) adj2 (linear adj	L24 0 (glass adj window)adj (linear adj expansion) L25 0 (glass adj window) adj2 (linear adj expansion) L26 0 (glass adj2 window) adj2 (linear adj expansion) L27 0 (glass adj2 window) adj4 (linear adj expansion) L28 63 glass and window and package and (linear adj2 expansion) L29 0 semiconductor and (glass adj lens) adj3 (linear adj expansion) L30 43 semiconductor and (glass adj2 lens) and (linear adj expansion) L31 1 chips and (glass adj2 lens) and (linear adj expansion) L32 0 (linear adj expansion) L32 0 (linear adj expansion adj "120")and window L33 0 (linear adj expansion adj "100")and window L34 0 semiconductor and (glass adj expansion) L35 14 semiconductor and glass adj3 (linear adj expansion) L36 0 (glass adj2 window) adj4 (linear adj expo) L37 0 (glass adj2 window) adj4 (linear adj expo) EPO; JPO	L24 0 (glass adj window)adj (linear adj expansion) L25 0 (glass adj window) adj2 (linear adj USPAT OR expansion) L26 0 (glass adj2 window) adj2 (linear adj USPAT OR expansion) L27 0 (glass adj2 window) adj4 (linear adj USPAT OR expansion) L28 63 glass and window and package and (linear adj2 expansion) L29 0 semiconductor and (glass adj lens) adj3 (linear adj expansion) L30 43 semiconductor and (glass adj2 lens) and (linear adj expansion) L31 1 chips and (glass adj2 lens) and (linear adj expansion) L32 0 (linear adj expansion) L32 0 (linear adj expansion adj "120")and window L33 0 (linear adj expansion adj "120")and window L34 0 semiconductor and (glass adj expansion) L35 14 semiconductor and (glass adj expansion) L36 0 (glass adj2 window) adj4 (linear adj EPO; JPO OR expansion) L37 0 (glass adj2 window) adj2 (linear adj EPO; JPO OR EPO; JPO OR

.